

PATENT NUMBER

U.S. UTILITY Patent Application

m/r O.I.P.E
 SCANNED KCS

PATENT DATE

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/989372	D	257	256	2814	T. DOAN

APPLICANTS

Richard Lane

TITLE

Method of patterning noble metals for semiconductor devices by electropolishing

PTO-2040
12/89

ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) _____ (Date)			NOTICE OF ALLOWANCE MAILED _____	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent, no. _____ _____ _____	_____ (Primary Examiner) _____ (Date)			ISSUE FEE Amount Due _____ Date Paid _____	
	_____ (Legal Instruments Examiner) _____ (Date)			ISSUE BATCH NUMBER _____	
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